



PATENT APPLICATION

Group Art Unit: 2816

Docket No.: 122358

T. LAM

Examiner:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Eiichi TAKAHASHI et al.

Application No.: 10/520,429

Filed: July 13, 2005

DIGITAL CIRCUIT HAVING DELAY CIRCUIT FOR ADJUSTMENT OF CLOCK

SIGNAL TIMING

<u>AMENDMENT</u>

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

For:

In reply to the January 29, 2007 Office Action, please consider the following:

Amendments to the Claims as reflected in the listing of claims;

Amendments to the Drawings include an attached replacement sheet; and

Remarks.